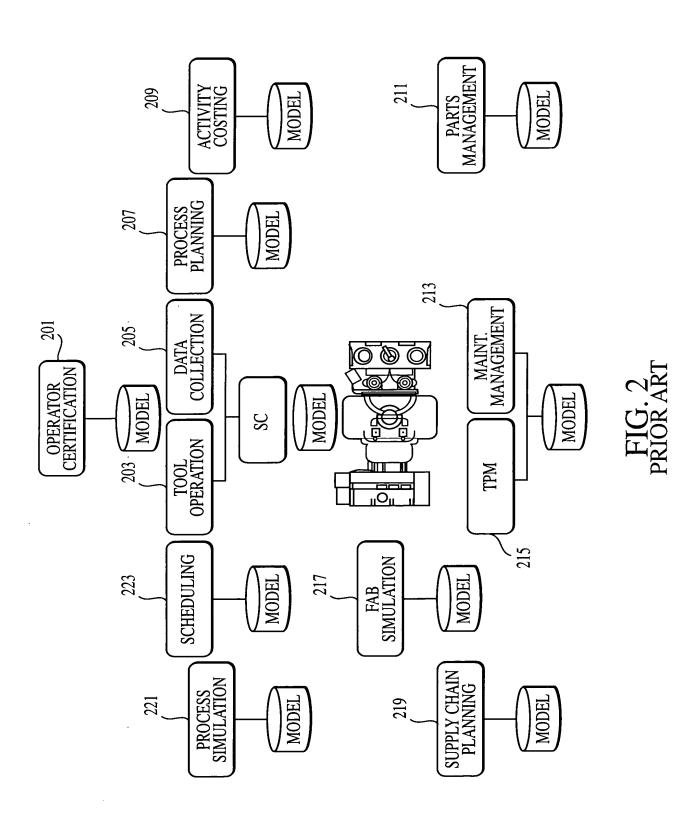


FIG. 1 PRIOR ART



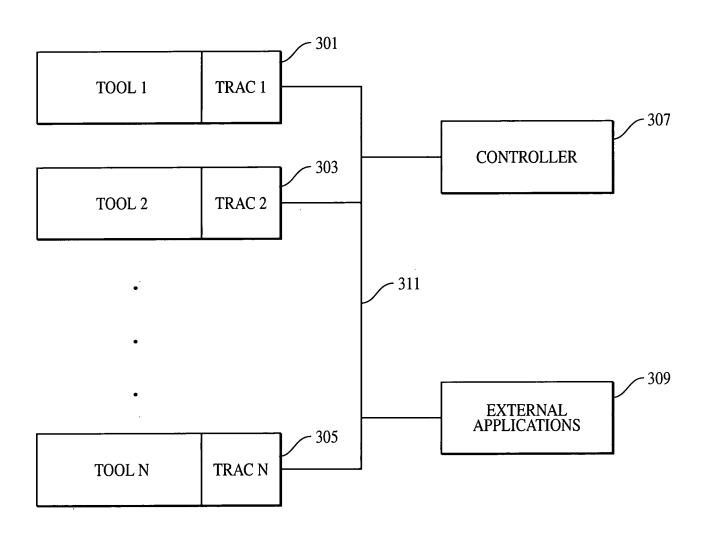


FIG. 3

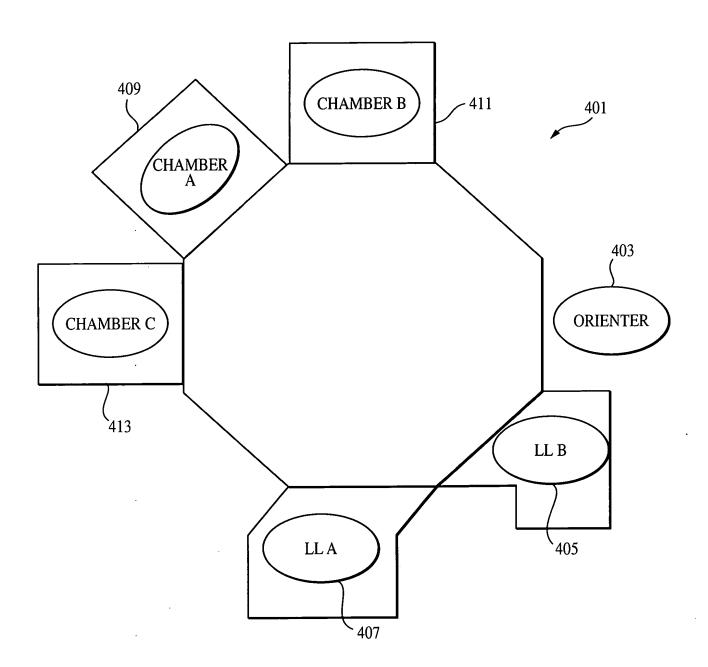


FIG. 4

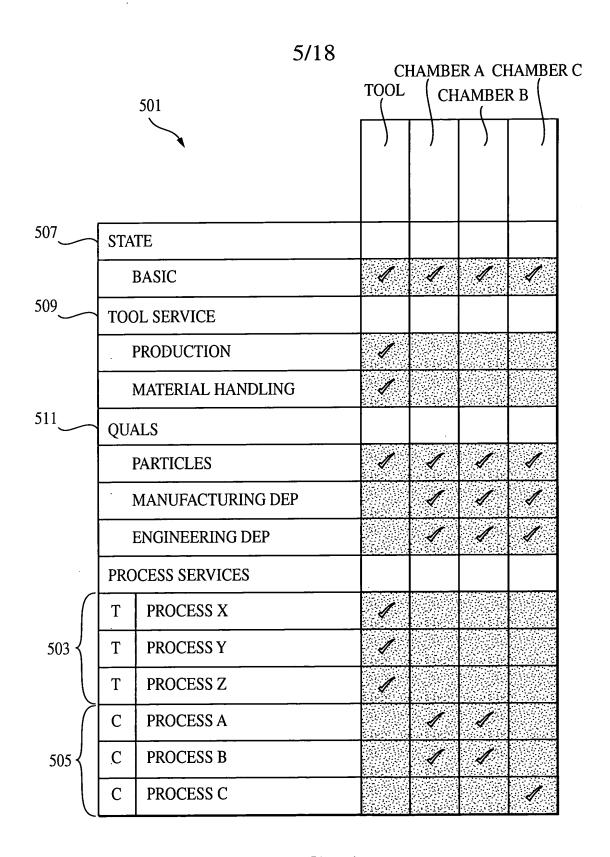


FIG. 5

	6/18	TOOL	CHAMBER A CHAMBER OOL (CHAMBER B		
)))	
QoS - CpK					
PROCESS SERVICES					
PROCESS X					
PROCESS Y					
PROCESS Z					
PROCESS A			2.1	2.16	
PROCESS B			3.03	3.21	
PROCESS C					4.16

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FIG. 6

	CHAMBER A CHAMBE TOOL CHAMBER B
QoS - PROC. TIME	
PROCESS SERVICES	
PROCESS X	
PROCESS Y	
PROCESS Z	
PROCESS A	42 43
PROCESS B	36 41
PROCESS C	130

FIG. 7

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QoS	- СрК	TOOL 1	TOOL 2	TOOL	TOOL 4
PRO	CESS SERVICES				
	PROCESS X	2.35	2.1	2.16	X
·	PROCESS Y	3.41	3.03	3.21	×
	PROCESS Z	X	4.19	4.21	4.16

FIG. 8

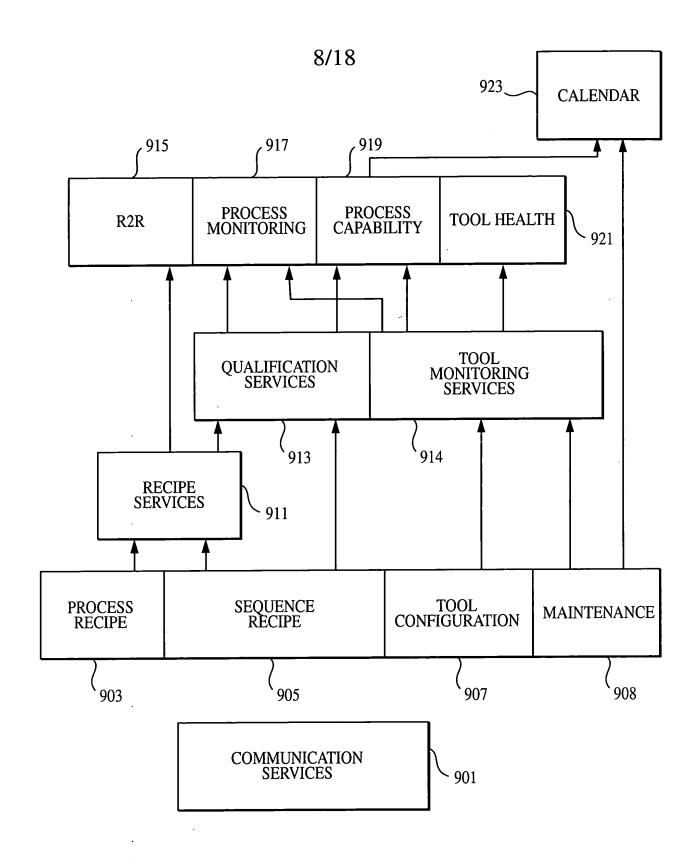
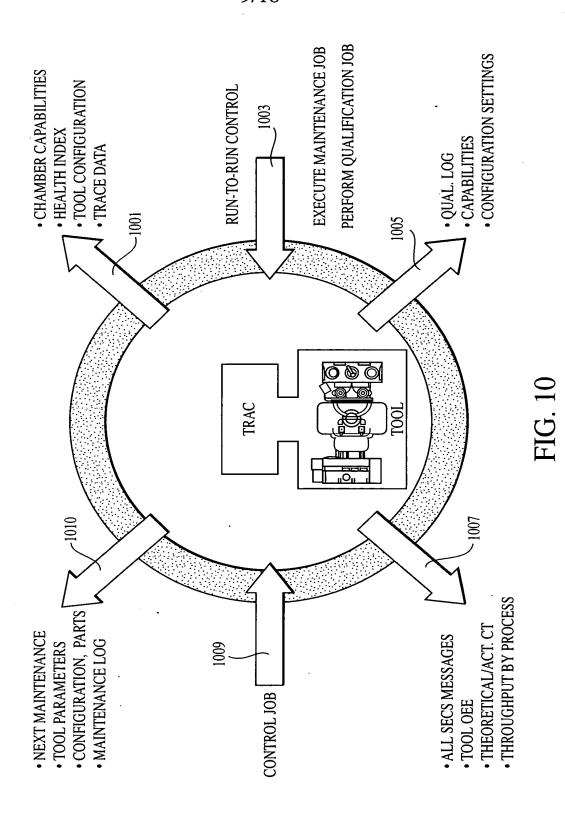


FIG. 9



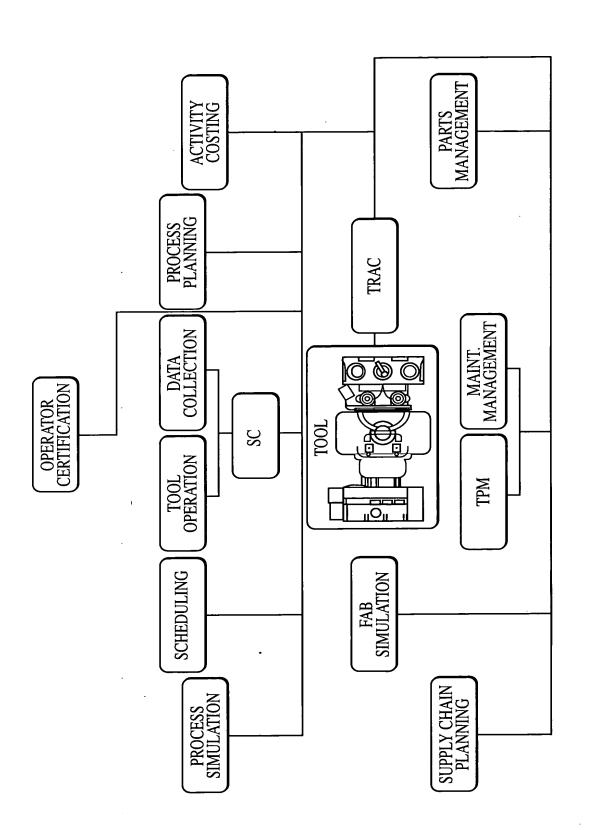


FIG. 11

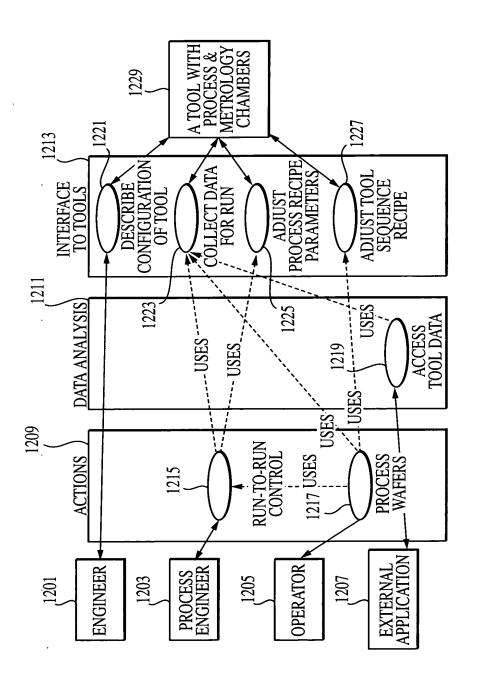


FIG. 12

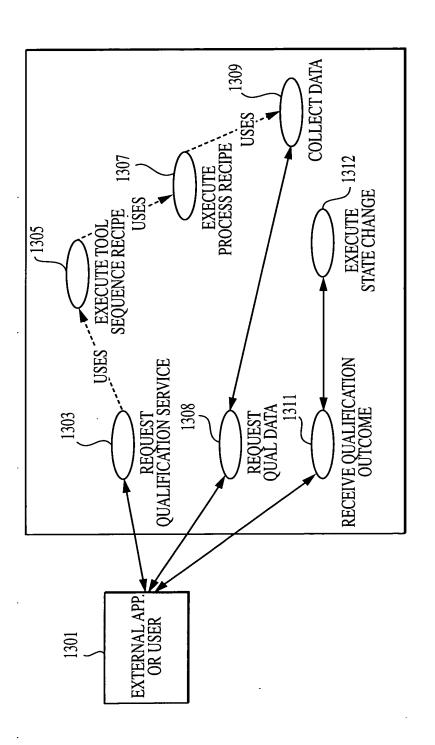


FIG. 13

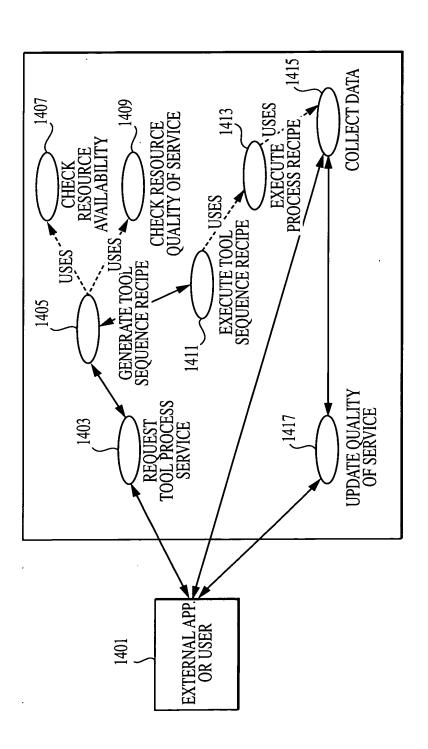


FIG. 14

1501	14/18	
EXTERNAL APPLICATION S2F41	<u>TRAC</u>	TOOL
PP SELECT S2F41 LOT START		THE SYSTEM INITIATES THE LOADING AND MAPPING OF THE WAFERS. AT COMPLETION THE SYSTEM SENDS
◀	(R2R USES THIS AS LOT INFO EVENT)	S6F11 PROCESS START FOR PORT (LOT ID, SET OF WAFER ID'S, SET OF PROCESS RECIPES IN SEQUENCE RECIPE)
·	CREATE EVENT REPORT(S). S2F35 LINK EVENT REPORT(S). S2F37 ENABLE EVENT REPORT(S). S2F49 ENABLES MODULE CONTROFOR SPECIFIED CHAMBER(S)	
	(R2R USES THIS AS SEND RECIPE EVENT) S2F49	TX PROCESS DELAY TIMER IS INITIATED S6F11 WAFER DESTINATION REPORTED AS SOON AS THE TOOL KNOWS WHICH CHAMBER THE WAFER IS GOING TO.
	RECIPE MODIFICATIONS ARE SENT TO THE TOOL.	IF TIMER EXPIRES ERROR HANDLING DEPENDENT ON TOOL CONFIGURATION
	(R2R USES THIS AS A RUN START EVENT)	S6F11 CHAMBER START EACH WAFER WILL GENERATE THIS EVENT EACH TIME A WAFER ARRIVES AT A PROCESS CHAMBER.
	(R2R USES THIS AS SEND PLANNED INPUT)	S6F11 PROCESS STEP COMPLETE SENDS AVERAGE PROCESS DATA FOR A SPECIFIC STEP. S6F11 PROCESS COMPLETE
	(R2R USES THIS AS A RUN STOP EVENT)	EACH WAFER WILL GENERATE THIS EVENT WHEN IT FINISHES <u>PROCESSING</u> IN EACH CHAMBER.
FIG. 15	(R2R USES THIS AS LOT DONE EVENT)	S6F11 LOT COMPLETE THIS EVENT REPORTS ONCE PER LOT, WHEN THE FINAL WAFER ARRIVES BACK IN THE LOAD LOCK.

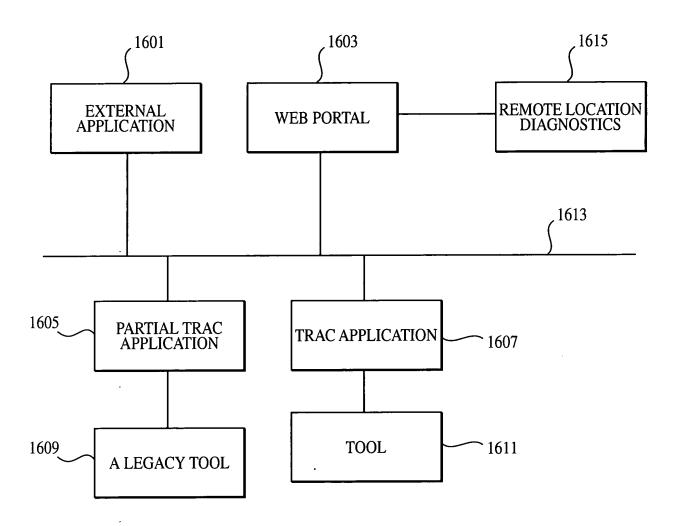


FIG. 16a

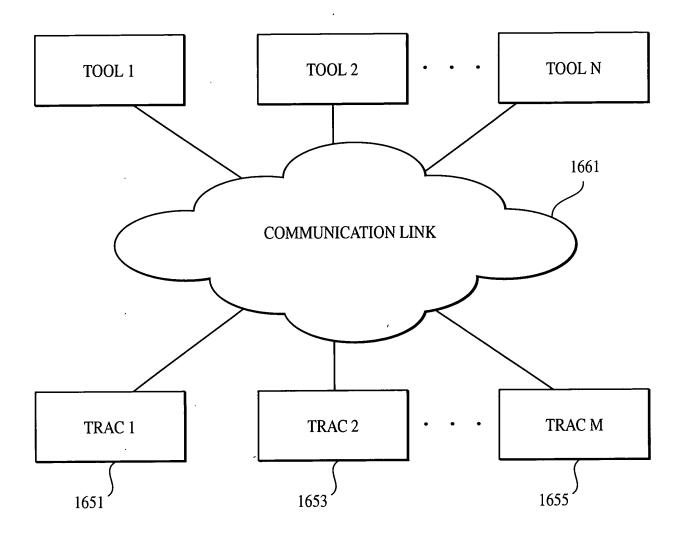


FIG. 16b

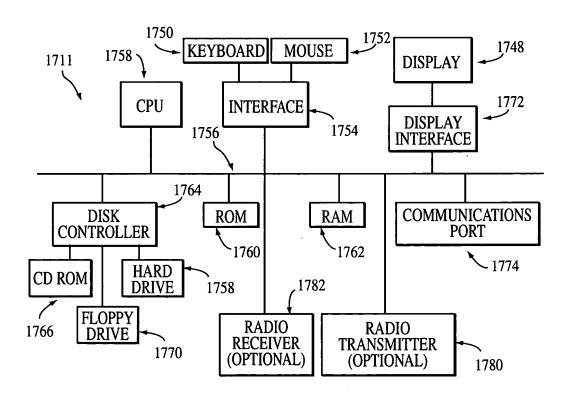


FIG. 17

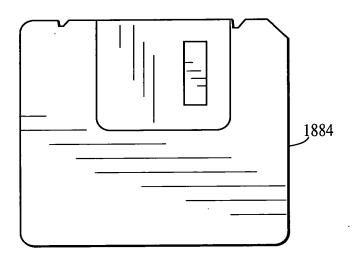


FIG. 18